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CONFIRMATION NO. 6665

Bib Data Sheet

SERIAL NUMBER 10/650,296	FILING DATE 08/27/2003 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 03-0861 81580
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APPLICANTS

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** CONTINUING DATA *N. VS*** FOREIGN APPLICATIONS *N. VS*

IF REQUIRED, FOREIGN FILING LICENSE

GRANTED

** 11/18/2003

Foreign Priority claimed	<input type="checkbox"/> yes	<input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes	<input type="checkbox"/> no	Met after	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<i>Vladi Salk</i>	<i>VS</i>	Allowance Initials	9	18	2

ADDRESS

LSI Logic Corporation
 Corporate Legal Department
 Intellectual Property Services Group
 1551 McCarthy Blvd., M/S D-106
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TITLE

Method of clock driven cell placement and clock tree synthesis for integrated circuit design

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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